

JFG-1

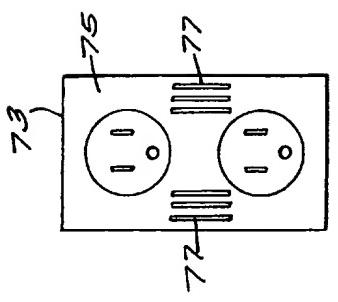
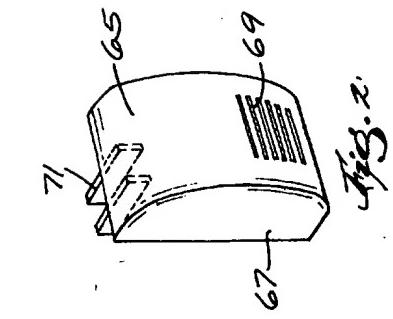


Fig. 3

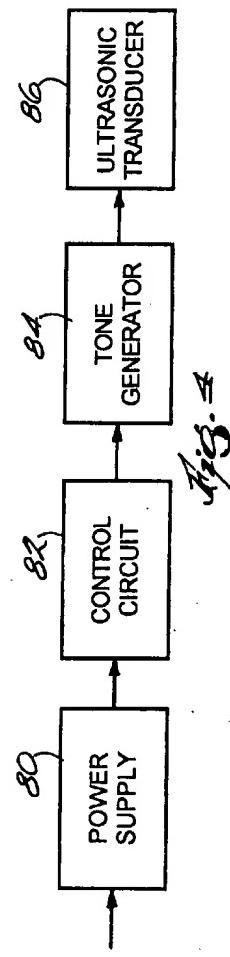


Fig. 4

Fig. 5

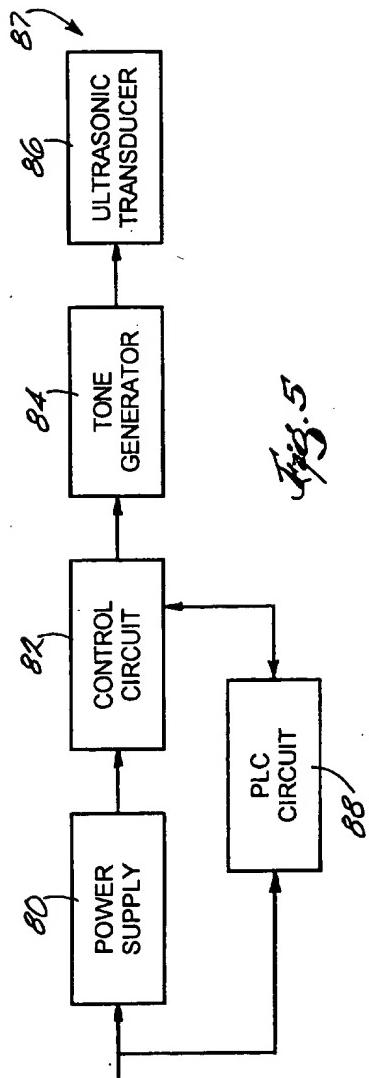
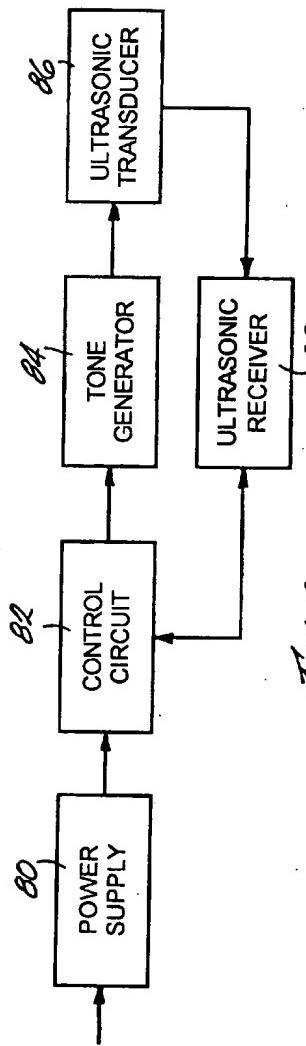


Fig. 6



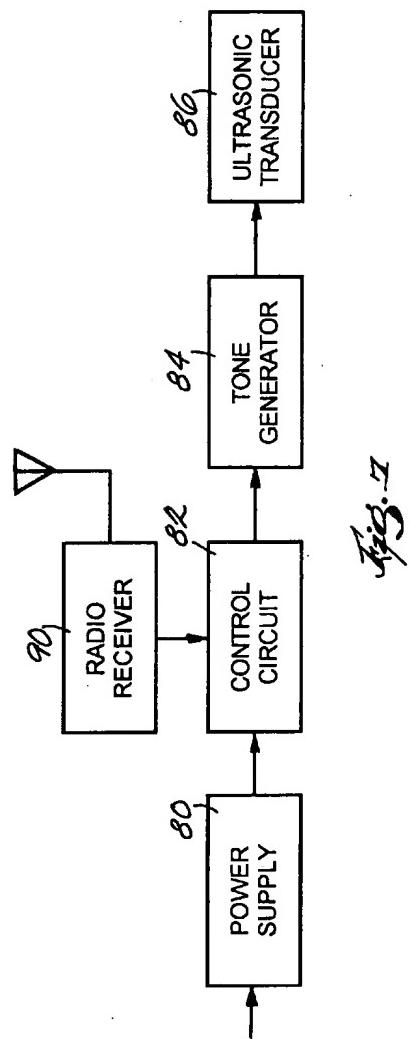


Fig. 7 Fig. 8 Fig. 9 Fig. 10

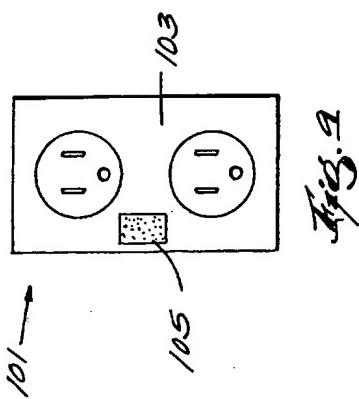
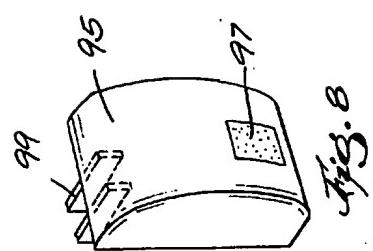


Fig. 9

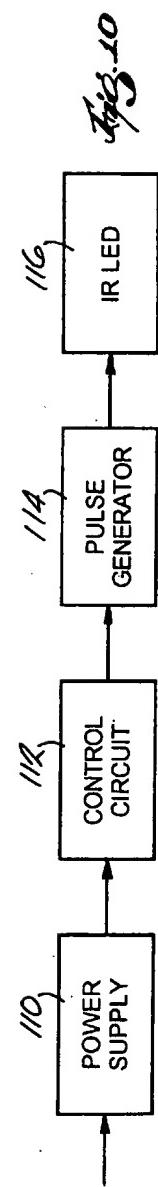


Fig. 10

F D D T S D " F E G T S G G E D

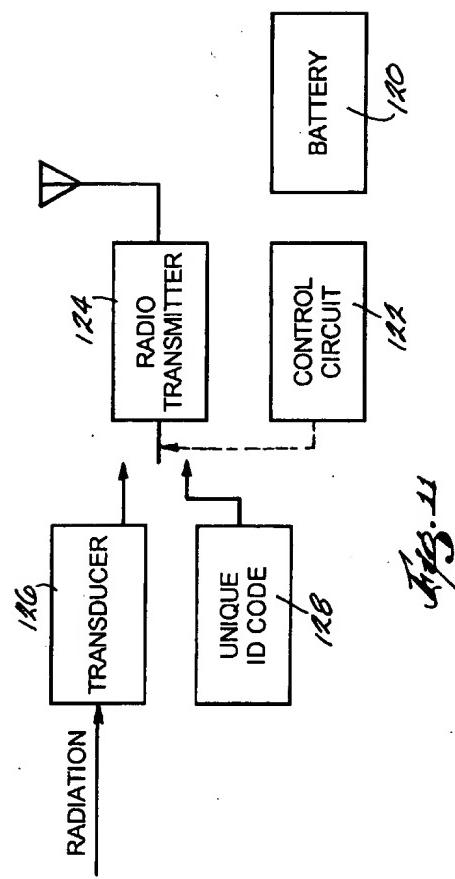


Fig. 11

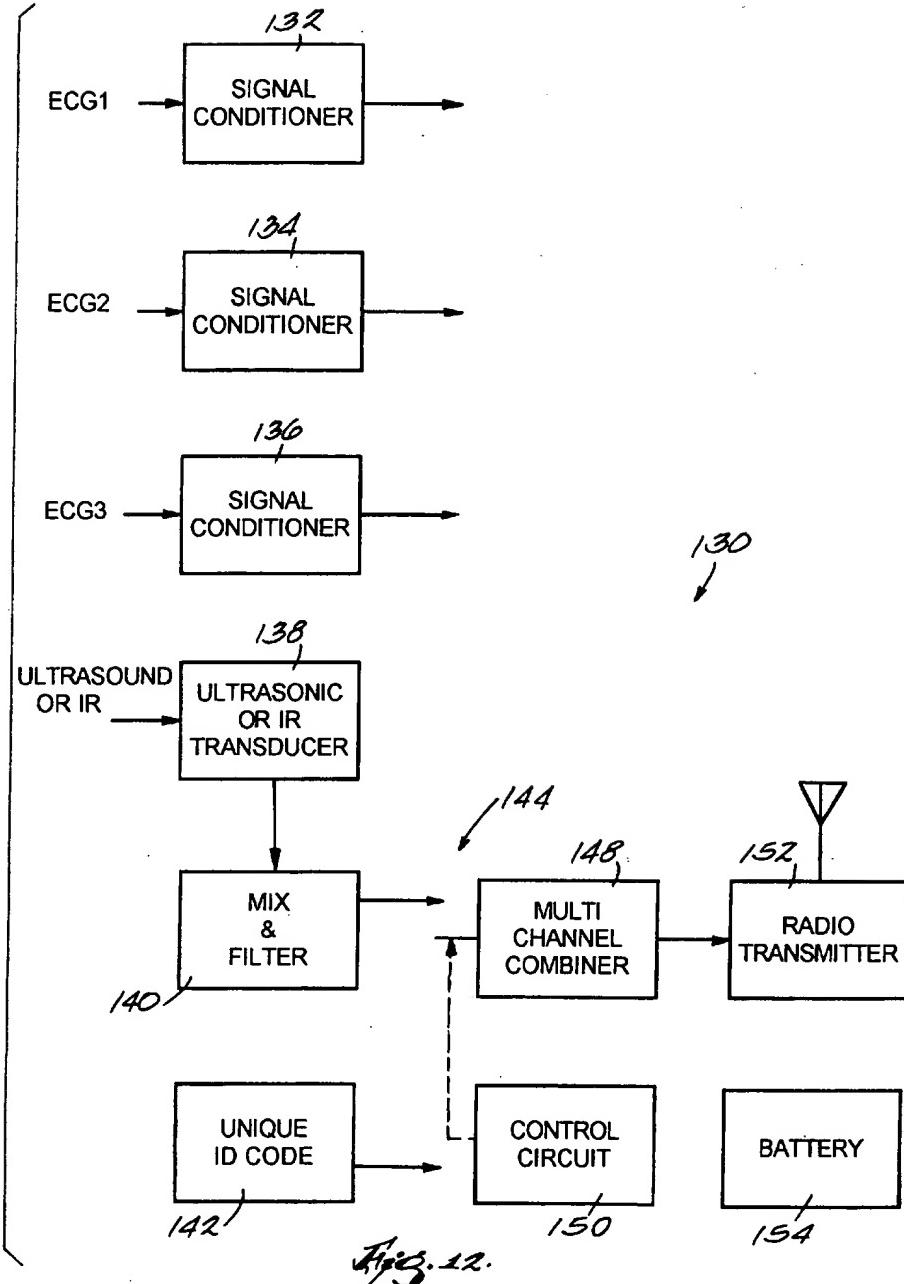
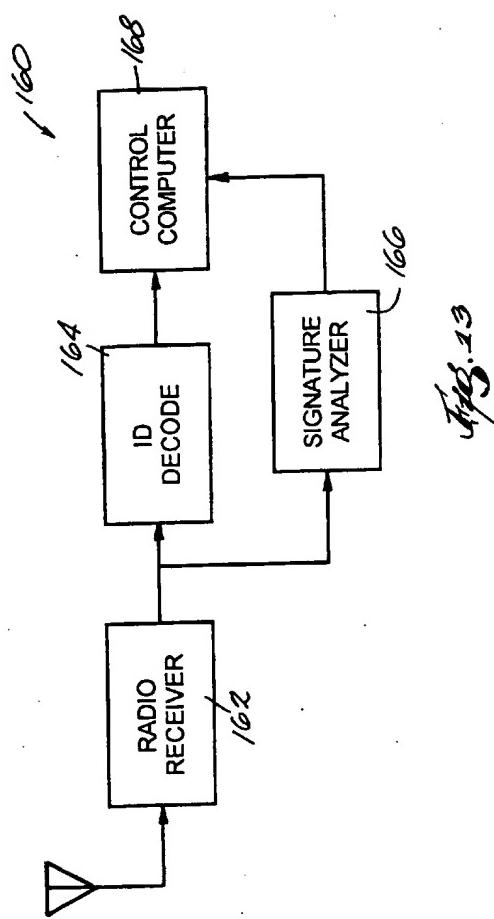


Fig. 13



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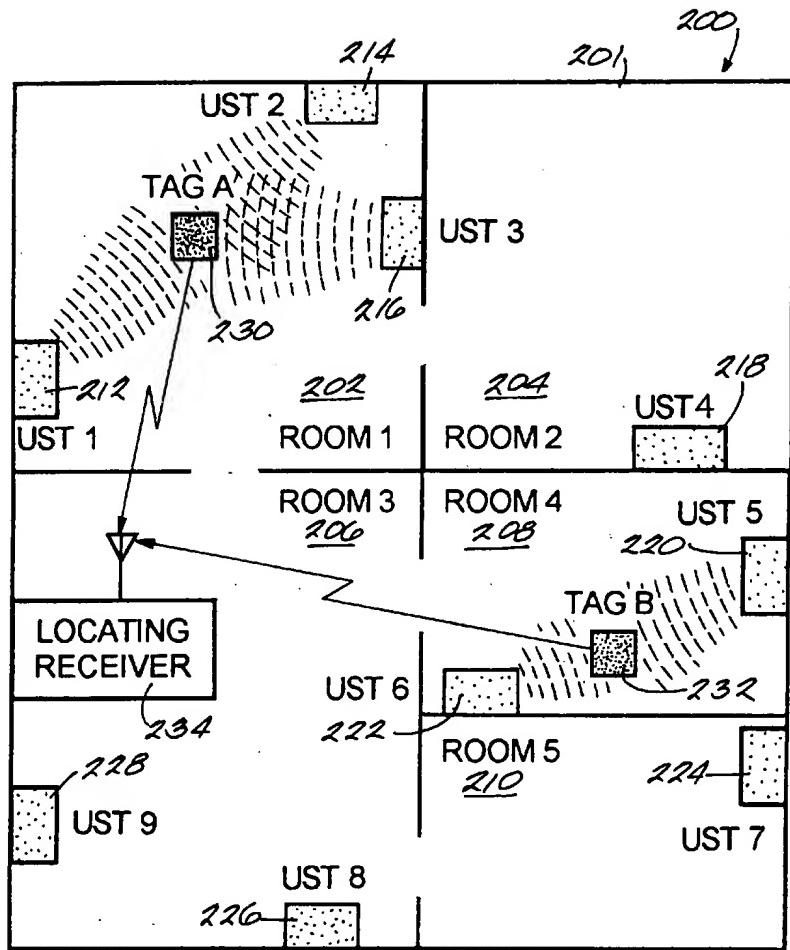


Fig. 14

00000000000000000000000000000000

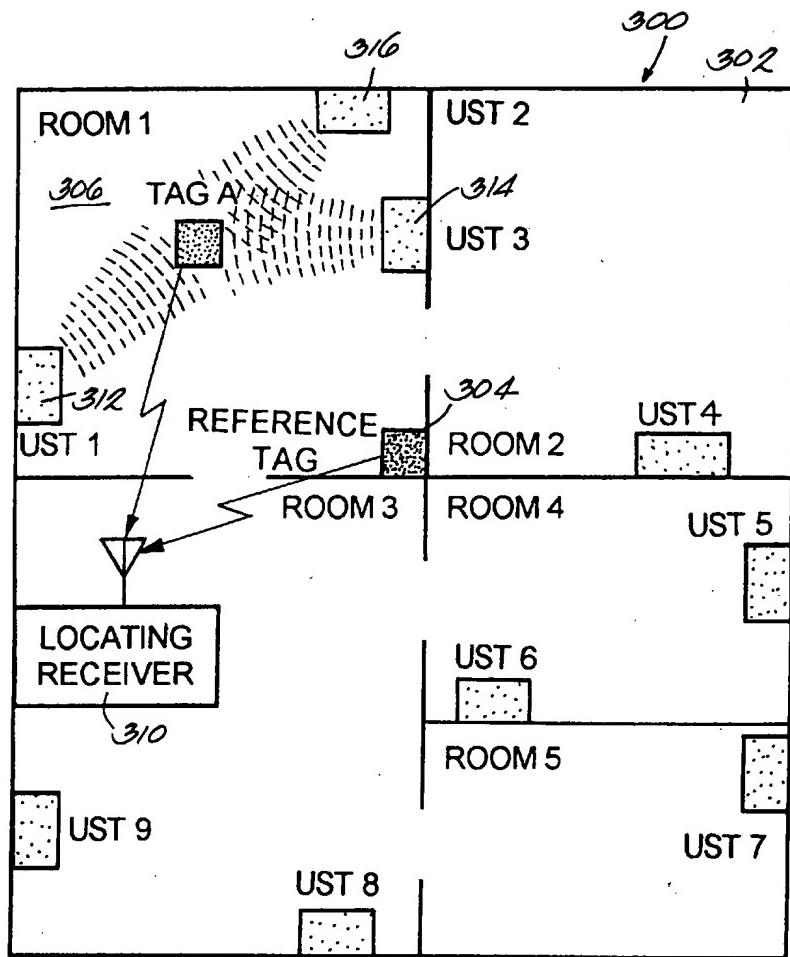


Fig. 16

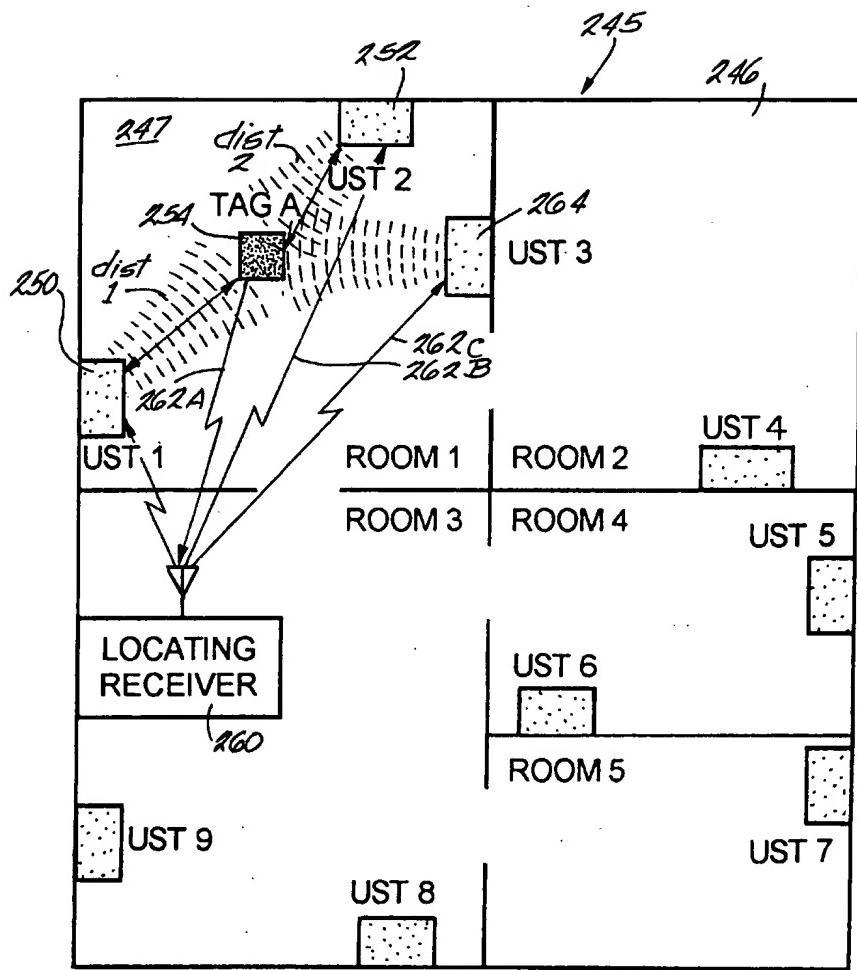


Fig. 15

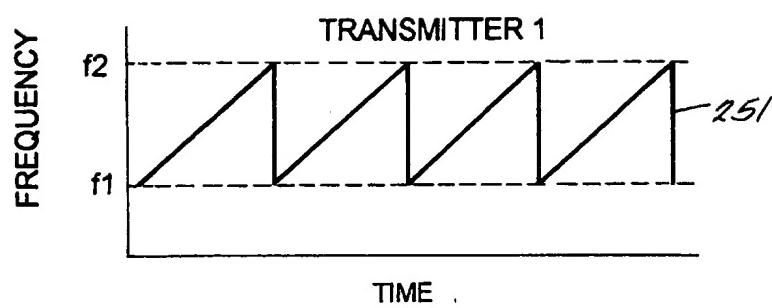


Fig. 17

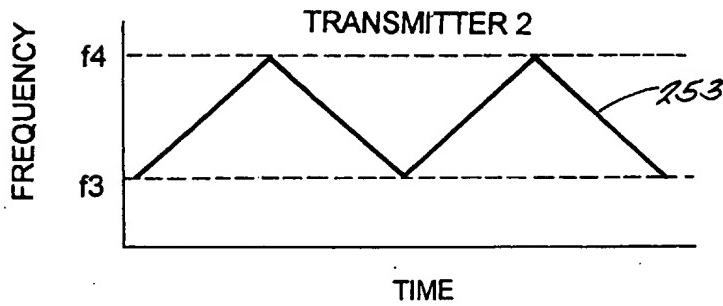


Fig. 18

Fig. 19

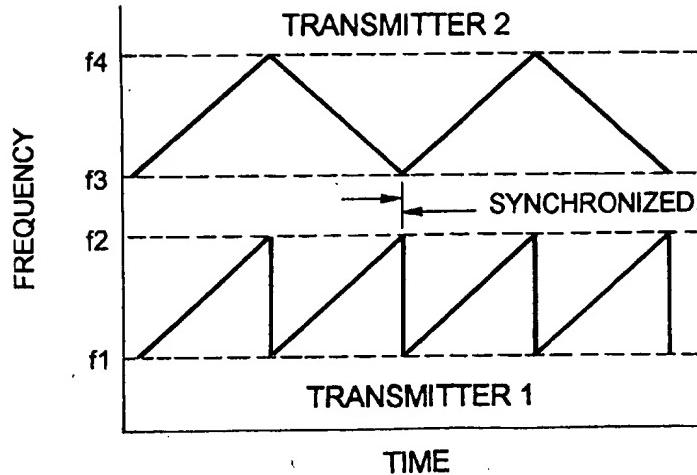


Fig. 19

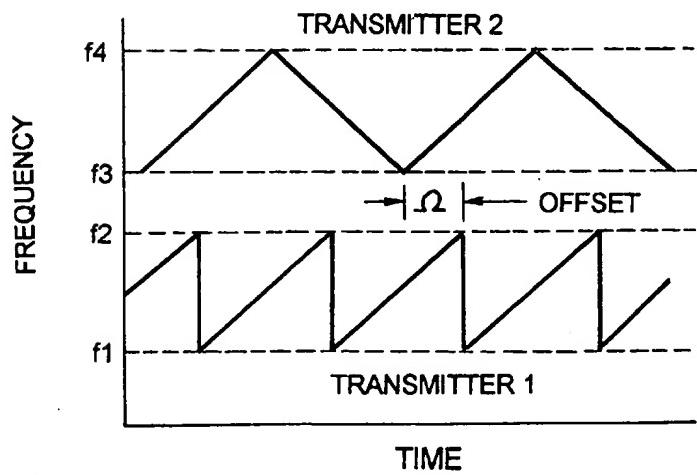


Fig. 20

100 100 100 100 100 100

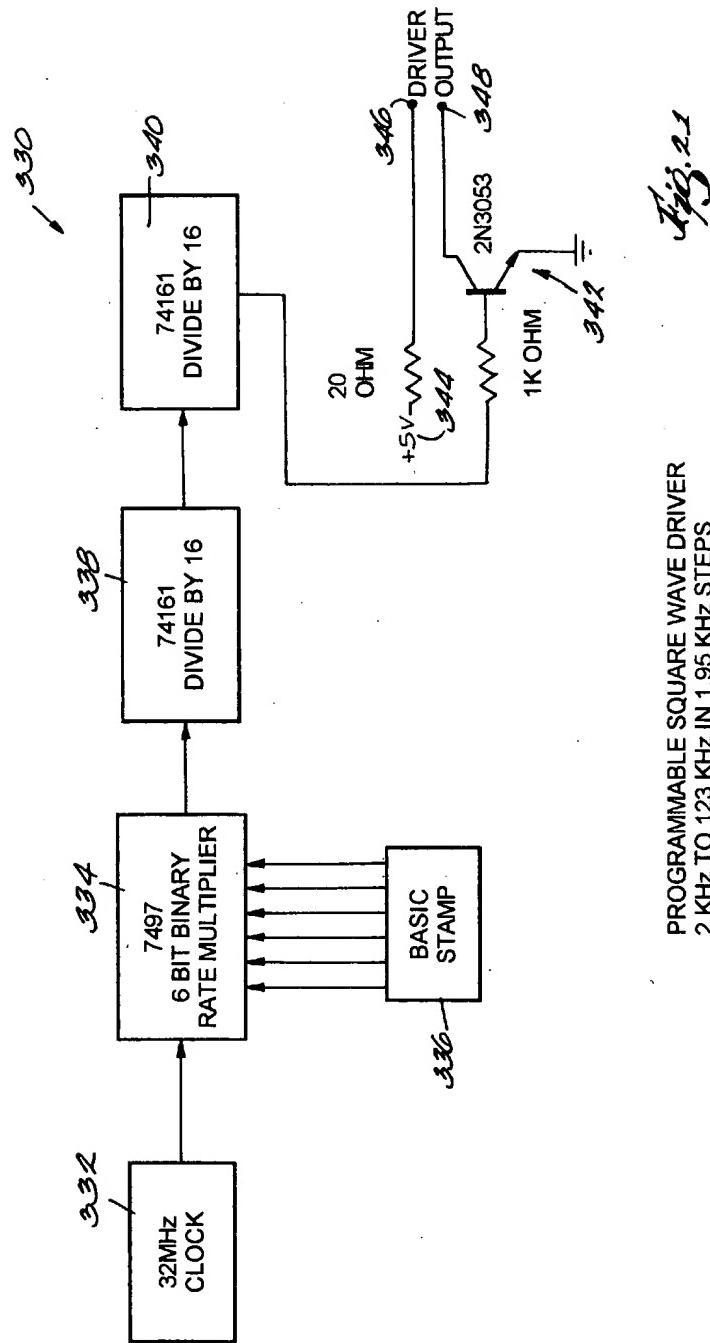


Fig. 2.1

PROGRAMMABLE SQUARE WAVE DRIVER
2 KHz TO 123 KHz IN 1.95 KHz STEPS

F D T E D T F D Y D G D

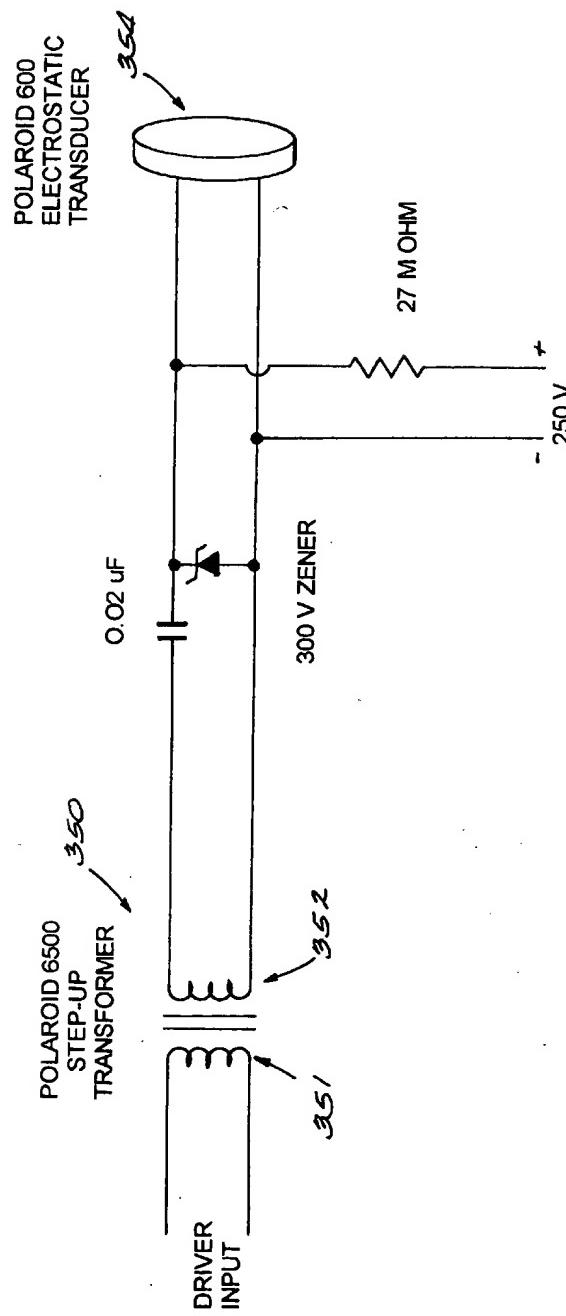


Fig. 22

TRANSDUCER INTERFACE CIRCUIT